CSc 553 — Principles of Compilation

37: Parallelizing Compilers II

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1 An Example (a)

```
FOR i := 2 TO 7 DO
    a[i] := a[i]+c; b[i] := a[i-1]*b[i];
```

i	Time	Statement
2	1	a[2]:=a[2]+c
	2	b[2]:=a[1]*b[2]
3	3	a[3]:=a[3]+c
	4	b[3]:=a[2]*b[3]
4	5	a[4]:=a[4]+c
	6	b[4]:=a[3]*b[4]
5	\overline{O}	a[5]:=a[5]+c
	8	b[5]:=a[4]*b[5]
6	9	a[6]:=a[6]+c
	A	b[6]:=a[5]*b[6]
7	B	a[7]:=a[7]+c
	C	b[7]:=a[6]*b[7]

2 An Example (b)

• Schedule the iterations of the following loop onto three CPUs (P_1, P_2, P_3) using cyclic scheduling.

```
FOR i := 2 TO 7 DO

S_1: a[i] := a[i] + c;

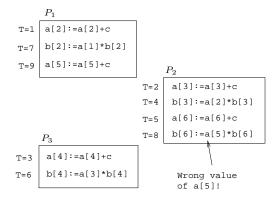
S_2: b[i] := a[i-1]*b[i];

ENDFOR
```

CPU	i	S_1	S_2
P_1	2	a[2]:=a[2]+c	b[2]:=a[1]*b[2]
	5	a[5]:=a[5]+c	b[5]:=a[4]*b[5]
P_2	3	a[3]:=a[3]+c	b[3]:=a[2]*b[3]
	6	a[6]:=a[6]+c	b[6]:=a[5]*b[6]
P_3	4	a[4]:=a[4]+c	b[4]:=a[3]*b[4]
	7	a[7]:=a[7]+c	b[7]:=a[6]*b[7]

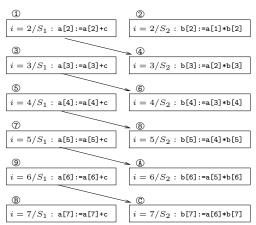
3 An Example (c)

- The three CPUs run asynchronously at different speeds. So, when P_2 is executing b[6]:=a[5]*b[6] at time T=8, P_1 has yet to execute a[5]:=a[5]+c.
- Hence, P_2 will be using the old (wrong) value of a[5].



4 An Example (d)

• Statement i/S_1 : a[i]:=a[i]+c must run before statement $i+1/S_2$: b[i]:=a[i-1]*b[i] in the next iteration.



5 Parallelizing Options I

• Approaches to fixing the problem:

- 1. Give up, and run the loop serially on one CPU.
- 2. Rewrite the loop to make it parallelizable.
- 3. Insert synchronization primitives.

Give up

• We should notify the programmer why the loop could not be parallelized, so maybe he/she can rewrite it him/herself.

```
Rewrite the loop
```

```
FOR i := 2 TO 7 DO

S_1: a[i] := a[i] + c;

ENDFOR;

FOR i := 2 TO 7 DO

S_2: b[i] := a[i-1]*b[i];

ENDFOR
```

6 Parallelizing Options II

Synchronize w/ Event Counters

```
VAR ev : EventCounter;

FOR i := 2 TO 7 DO

S_1: a[i] := a[i] + c;

advance(ev); await(ev, i-1)

S_2: b[i] := a[i-1]*b[i];

ENDFOR
```

- await/advance implements an ordered critical section, a region of code that the Workers must enter in some particular order.
- await/advance are implemented by means of an event counter, an integer protected by a lock.
- await(ev, i) sleeps until the event counter reaches i.
- advance(ev) increments the counter.

7 Parallelizing Options III

Synchronize w/ Vectors

```
VAR ev : SynchronizationVector;

FOR i := 2 TO 7 DO

S_1: a[i] := a[i] + c;

ev[i] := 1;

IF i > 2 THEN

wait(ev[i-1])

ENDIF;

S_2: b[i] := a[i-1]*b[i];

ENDFOR
```

- ev is a vector of bits, one per iteration. It is protected by a lock and initialized to all 0's.
- wait(ev[i]) will sleep the process until ev[i]=1.
- Initialization of the vector can be expensive.

8

What does a real compiler do?

9 pca's Choices I (a)

- Let's see how pca treats this loop.
- pca -unroll=1 -cmp -lo=cklnps -list=1.1 l.c

C Program in 1.c

```
int i,n; double a[10000], b[10000];
main () {
  for(i=2; i<=n; i++) {
    a[i] = a[i] + 100.0;
    b[i] = a[i-1]*b[i]; }}
```

Listing in 1.1

for i

Original loop split into sub-loops

- 1. Concurrent
- 2. Concurrent

1 loops concurrentized

10 pca's Choices I (b)

Parallelized program in 1.m

pca's Choices II (a) 11

}

• Let's try a slightly different loop....

```
C Program in d.c
   for(i=2; i<=n; i++) {</pre>
      a[i] = a[i+1] + 100.0;
      b[i] = a[i-1]*b[i];
                                           Listing in d.1
for i
    Original loop split into sub-loops
    1. Scalar
```

```
Data dependence involving this
     line due to variable "a"
2. Concurrent
```

1 loops concurrentized

pca's Choices II (b) 12

Parallelized program in d.m

```
for ( K1 = 2; K1<=n; K1++ )</pre>
   a[K1] = a[K1+1] + 100.0;
#pragma parallel if(n > 102) byvalue(n)
         shared(a, b) local(K1)
#pragma pfor iterate(K1=2;n-1;1)
   for ( K1 = 2; K1<=n; K1++ )</pre>
      b[K1] = a[K1-1] * b[K1];
}
```

• This time pca

1. split the loop in two subloops (like before),

- 2. parallelized the second subloop, and
- 3. gave up on the first subloop, executing it serially.

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Concurrentization

14 Concurrentization

- A loop can be concurrentized iff all its data dependence directions are =.
- In other words, a loop can be concurrentized iff it has no loop carried data dependences.

• The *I*-loop below cannot be directly concurrentized. The loop dependences are $S_1 \ \delta_{=,<} S_1$, $S_1 \ \delta_{=,=} S_2$, $S_2 \ \overline{\delta}_{<,=} S_3$. Hence, the *I*-loop's dependence directions are (=,=,<).

15 Exam I (415.730/96)

```
FOR i := 1 TO n DO

FOR j := 1 TO n DO

S_1: \quad A[i, j] := A[i, j - 1] + C;

END;

END;
```

- 1. Which of the dependencies are **loop**-carried?
- 2. Which of the loops can be directly concurrentized (i.e., run in parallel without any loop transformations or extra synchronization)? Motivate your answer!
- 3. What is the difference between a pre-scheduled and a self-scheduled loop? Under what circumstances should we prefer one over the other?

16 Readings and References

 Padua & Wolfe, Advanced Compiler Optimizations for Supercomputers, CACM, Dec 1996, Vol 29, No 12, pp. 1184–1187.

17 Summary I

- Dependence analysis is an important part of any parallelizing compiler. In general, it's a very difficult problem, but, fortunately, most programs have very simple index expressions that can be easily analyzed.
- Most compilers will try to do a good job on **common** loops, rather than a half-hearted job on all loops.

18 Summary II

• When faced with a loop

```
FOR i := From TO To DO

S_1: \quad \mathbf{A}[f(i)] := \cdots

S_2: \quad \cdots := \mathbf{A}[g(i)]

ENDFOR
```

the compiler will try to determine if there are any index values I, J for which f(I) = g(J). A number of cases can occur:

- 1. The compiler decides that f(i) and g(i) are too complicated to analyze. \Rightarrow Run the loop serially.
- 2. The compiler decides that f(i) and g(i) are very simple (e.g. f(i)=i, f(i)=c*i, f(i)=i+c, f(i)=c*i+d), and does the analysis using some built-in pattern matching rules. ⇒ Run the loop in parallel or serially, depending on the outcome.