# CSc 553 - Principles of Compilation 

37 : Parallelizing Compilers II

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## 1 An Example (a)

FOR i := 2 TO 7 DO
$\mathrm{a}[\mathrm{i}]:=\mathrm{a}[\mathrm{i}]+\mathrm{c} ; \mathrm{b}[\mathrm{i}]:=\mathrm{a}[\mathrm{i}-1] * \mathrm{~b}[\mathrm{i}]$;

| i | Time | Statement |
| :--- | :--- | :--- |
| 2 | $(1)$ | $\mathrm{a}[2]:=\mathrm{a}[2]+\mathrm{c}$ |
|  | $(2)$ | $\mathrm{b}[2]:=\mathrm{a}[1] * \mathrm{~b}[2]$ |
| 3 | $(3)$ | $\mathrm{a}[3]:=\mathrm{a}[3]+\mathrm{c}$ |
|  | $(4)$ | $\mathrm{b}[3]:=\mathrm{a}[2] * \mathrm{~b}[3]$ |
| 4 | $(5)$ | $\mathrm{a}[4]:=\mathrm{a}[4]+\mathrm{c}$ |
|  | $(6)$ | $\mathrm{b}[4]:=\mathrm{a}[3] * \mathrm{~b}[4]$ |
| 5 | $(7)$ | $\mathrm{a}[5]:=\mathrm{a}[5]+\mathrm{c}$ |
|  | $(8)$ | $\mathrm{b}[5]:=\mathrm{a}[4] * \mathrm{~b}[5]$ |
| 6 | $(9)$ | $\mathrm{a}[6]:=\mathrm{a}[6]+\mathrm{c}$ |
|  | (A) | $\mathrm{b}[6]:=\mathrm{a}[5] * \mathrm{~b}[6]$ |
| 7 | (B) | $\mathrm{a}[7]:=\mathrm{a}[7]+\mathrm{c}$ |
|  | C | $\mathrm{b}[7]:=\mathrm{a}[6] * \mathrm{~b}[7]$ |

## 2 An Example (b)

- Schedule the iterations of the following loop onto three CPUs ( $P_{1}, P_{2}, P_{3}$ ) using cyclic scheduling.

```
FOR i := 2 TO 7 DO
    S : a[i] := a[i] + c;
    S2: b[i] := a[i-1]*b[i];
ENDFOR
```

| CPU | i | $S_{1}$ | $S_{2}$ |
| :--- | :---: | :---: | :---: |
| $P_{1}$ | 2 | $\mathrm{a}[2]:=\mathrm{a}[2]+\mathrm{c}$ | $\mathrm{b}[2]:=\mathrm{a}[1] * \mathrm{~b}[2]$ |
|  | 5 | $\mathrm{a}[5]:=\mathrm{a}[5]+\mathrm{c}$ | $\mathrm{b}[5]:=\mathrm{a}[4] * \mathrm{~b}[5]$ |
| $P_{2}$ | 3 | $\mathrm{a}[3]:=\mathrm{a}[3]+\mathrm{c}$ | $\mathrm{b}[3]:=\mathrm{a}[2] * \mathrm{~b}[3]$ |
|  | 6 | $\mathrm{a}[6]:=\mathrm{a}[6]+\mathrm{c}$ | $\mathrm{b}[6]:=\mathrm{a}[5] * \mathrm{~b}[6]$ |
| $P_{3}$ | 4 | $\mathrm{a}[4]:=\mathrm{a}[4]+\mathrm{c}$ | $\mathrm{b}[4]:=\mathrm{a}[3] * \mathrm{~b}[4]$ |
|  | 7 | $\mathrm{a}[7]:=\mathrm{a}[7]+\mathrm{c}$ | $\mathrm{b}[7]:=\mathrm{a}[6] * \mathrm{~b}[7]$ |

## 3 An Example (c)

- The three CPUs run asynchronously at different speeds. So, when $P_{2}$ is executing $\mathrm{b}[6]:=\mathrm{a}[5] * \mathrm{~b}[6]$ at time $\mathrm{T}=8, P_{1}$ has yet to execute a[5]:=a[5]+c.
- Hence, $P_{2}$ will be using the old (wrong) value of a [5].



## 4 An Example (d)

- Statement $i / S_{1}: \mathrm{a}[\mathrm{i}]:=\mathrm{a}[\mathrm{i}]+\mathrm{c}$ must run before statement $i+1 / S_{2}: \mathrm{b}[\mathrm{i}]:=\mathrm{a}[\mathrm{i}-1] * \mathrm{~b}[\mathrm{i}]$ in the next iteration.



## 5 Parallelizing Options I

- Approaches to fixing the problem:

1. Give up, and run the loop serially on one CPU.
2. Rewrite the loop to make it parallelizable.
3. Insert synchronization primitives.

- We should notify the programmer why the loop could not be parallelized, so maybe he/she can rewrite it him/herself.

Rewrite the loop

```
FOR i := 2 TO 7 DO
    S1: a[i] := a[i] + c;
ENDFOR;
FOR i := 2 TO 7 DO
    S : b [i] := a[i-1]*b[i];
ENDFOR
```


## 6 Parallelizing Options II

```
                                    Synchronize w/ Event Counters
VAR ev : EventCounter;
FOR i := 2 TO 7 DO
    S1: a[i] := a[i] + c;
        advance(ev); await(ev, i-1)
    S : b[i] := a[i-1]*b[i];
ENDFOR
```

- await/advance implements an ordered critical section, a region of code that the Workers must enter in some particular order.
- await/advance are implemented by means of an event counter, an integer protected by a lock.
- await (ev, i) sleeps until the event counter reaches i.
- advance(ev) increments the counter.


## 7 Parallelizing Options III

Synchronize w/ Vectors

```
VAR ev : SynchronizationVector;
FOR i := 2 TO 7 DO
    S1: a[i] := a[i] + c;
        ev[i] := 1;
        IF i > 2 THEN
            wait(ev[i-1])
        ENDIF;
    S : b [i] := a[i-1]*b[i];
ENDFOR
```

- ev is a vector of bits, one per iteration. It is protected by a lock and initialized to all 0 's.
- wait (ev[i]) will sleep the process until ev [i]=1.
- Initialization of the vector can be expensive.


## 8

## What does a real compiler do?

## 9 pca's Choices I (a)

- Let's see how pca treats this loop.
- pca -unroll=1 -cmp -lo=cklnps -list=l.l l.c

$$
工 \quad \text { C Program in l.c }
$$

```
int i,n; double a[10000], b[10000];
```

main () \{
for (i=2; i<=n; i++) \{
$\mathrm{a}[\mathrm{i}]=\mathrm{a}[\mathrm{i}]+100.0$;
$\mathrm{b}[\mathrm{i}]=\mathrm{a}[\mathrm{i}-1] * \mathrm{~b}[\mathrm{i}] ;\}\}$

```
for i
```

    Original loop split into sub-loops
    1. Concurrent
    2. Concurrent
        1 loops concurrentized
    
## 10 pca's Choices I (b)

```
                                    Parallelized program in 1.m
int main( ) {
    int K1, K3;
    K3 = ((n - 1)>(0) ? (n - 1) : (0));
#pragma parallel if(n > 51) byvalue(n)
            shared(a, b) local(K1) {
#pragma pfor iterate(K1=2;n-1;1)
        for ( K1 = 2; K1<=n; K1++ )
            a[K1] = a[K1] + 100.e0;
#pragma synchronize
#pragma pfor iterate(K1=2;n-1;1)
        for ( K1 = 2; K1<=n; K1++ )
            b[K1] = a[K1-1] * b[K1];
    }
    i = K3 + 2;
}
```


## 11 pca's Choices II (a)

- Let's try a slightly different loop....

```
    C Program in d.c
    for(i=2; i<=n; i++) {
        a[i] = a[i+1] + 100.0;
        b[i] = a[i-1]*b[i];
}
```

Listing in d.l
for i
Original loop split into sub-loops

1. Scalar Data dependence involving this line due to variable "a"
2. Concurrent

1 loops concurrentized

## 12 pca's Choices II (b)

```
                                    Parallelized program in d.m
for ( K1 = 2; K1<=n; K1++ )
    a[K1] = a[K1+1] + 100.0;
#pragma parallel if(n > 102) byvalue(n)
            shared(a, b) local(K1)
{
#pragma pfor iterate(K1=2;n-1;1)
    for (K1 = 2; K1<=n; K1++ )
        b[K1] = a[K1-1] * b[K1];
}
```

- This time pca

1. split the loop in two subloops (like before),
2. parallelized the second subloop, and
3. gave up on the first subloop, executing it serially.

## 13

## Concurrentization

## 14 Concurrentization

- A loop can be concurrentized iff all its data dependence directions are $=$.
- In other words, a loop can be concurrentized iff it has no loop carried data dependences.
- The $I$-loop below cannot be directly concurrentized. The loop dependences are $S_{1} \delta_{=,<} S_{1}, S_{1} \delta_{=,=} S_{2}$, $S_{2} \bar{\delta}_{<,=} S_{3}$. Hence, the $I$-loop's dependence directions are $(=,=,<)$.

```
FOR I := 1 TO N DO
    FOR J := 2 TO N DO
        S : A [I,J] := A[I,J - 1] + B[I,J];
        S2: C[I,J] := A[I,J] + D [I+1,J];
        S3: D [I,J] := 0.1;
    ENDFOR
ENDFOR
```


## 15 Exam I (415.730/96)

```
    FOR i := 1 TO n DO
    FOR j := 1 TO n DO
S : A A i,j] := A[i,j-1] + C;
    END;
    END;
```

1. Which of the dependencies are loop-carried?
2. Which of the loops can be directly concurrentized (i.e., run in parallel without any loop transformations or extra synchronization)? Motivate your answer!
3. What is the difference between a pre-scheduled and a self-scheduled loop? Under what circumstances should we prefer one over the other?

## 16 Readings and References

- Padua \& Wolfe, Advanced Compiler Optimizations for Supercomputers, CACM, Dec 1996, Vol 29, No 12, pp. 1184-1187.


## 17 Summary I

- Dependence analysis is an important part of any parallelizing compiler. In general, it's a very difficult problem, but, fortunately, most programs have very simple index expressions that can be easily analyzed.
- Most compilers will try to do a good job on common loops, rather than a half-hearted job on all loops.


## 18 Summary II

- When faced with a loop

```
FOR i := From TO To DO
    S : A [f(i)] := ...
    S2: .. := A[g(i)]
ENDFOR
```

the compiler will try to determine if there are any index values $I, J$ for which $f(I)=g(J)$. A number of cases can occur:

1. The compiler decides that $f(i)$ and $g(i)$ are too complicated to analyze. $\Rightarrow$ Run the loop serially.
2. The compiler decides that $f(i)$ and $g(i)$ are very simple (e.g. $\mathrm{f}(\mathrm{i})=\mathrm{i}, \mathrm{f}(\mathrm{i})=\mathrm{c} * \mathrm{i}, \mathrm{f}(\mathrm{i})=\mathrm{i}+\mathrm{c}$, $f(i)=c * i+d$, and does the analysis using some built-in pattern matching rules. $\Rightarrow$ Run the loop in parallel or serially, depending on the outcome.
