# CSc 553 - Principles of Compilation 

X16 : Optimization I

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Introduction
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## What do we Optimize?

## 4 What do we Optimize I?

1. Optimize everything, all the time. The problem is that optimization interferes with debugging. In fact, many (most) compilers don't let you generate an optimized program with debugging information. The problem of debugging optimized code is an important research field.

Furthermore, optimization is probably the most time consuming pass in the compiler. Always optimizing everything (even routines which will never be called!) wastes valuable time.
2. The programmer decides what to optimize. The problem is that the programmer has a local view of the code. When timing a program programmers are often very surprised to see where most of the time is spent.

## 5 What do we Optimize II?

3. Turn optimization on when program is complete. Unfortunately, optimizers aren't perfect, and a program that performed OK with debugging turned on often behaves differently when debugging is off and optimization is on.
4. Optimize inner loops only. Unfortunately, procedure calls can hide inner loops:
```
PROCEDURE P(n);
BEGIN
    FOR k:=1 TO n DO ... END;
END P;
FOR i:=1 TO 10000 DO P(i) END;
```


## 6 What do we Optimize III?

5. Use profiling information to guide what to optimize.

6. Runtime code generation/optimization. We delay code generation and optimization until execution time. At that time we have more information to guide the otpimizations:


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## Local vs. Global vs. Inter-procedural Optimization

## 8 Local, Global, Inter-Proc. I

- Some compilers optimize more aggressively than others. An aggressive compiler optimizes over a large piece of code, a simple one only considers a small chunk of code at a time.


## Local Optimization

- Consider each basic block by itself.
- All compilers do this.
- Consider each procedure by itself.
- Most compilers do this.
$\qquad$
- Consider the control flow between procedures.
- A few compilers do this.


## 9 Local Optimization I

Transformations: $\qquad$

- Local common subexpression elimination.
- Local copy propagation.
- Local dead-code elimination.
- Algebraic optimization.
- Jump-to-jump removal.
- Reduction in strength.
$\qquad$
- On machine and/or interm. code.

1. Examine a "window" of instructions.
2. Improve code in window.
3. Slide window.
4. Repeat until "optimal".

## 10 Local Opt. II



Redundant Loads: $\qquad$
$\qquad$ Jumps-to-jumps: $\qquad$
if $\mathrm{a}<\mathrm{b}$ goto L1 .....
L1: goto L2 .....
L2: goto L3 ..... $\Rightarrow$
if a < b goto L3 .....
L1: goto L3 .....
L2: goto L3 .....

## 11 Local Opt. IV


$\mathrm{x}:=\mathrm{x} * 32 ; \Rightarrow \mathrm{x}:=\operatorname{SHL}(\mathrm{x}, 5) ;$
$\mathrm{x}:=\mathrm{x} * 100 ; \Rightarrow$
$\mathrm{x}:=\mathrm{x} *(64+32+4) \Rightarrow$
$\mathrm{x}:=\mathrm{x} * 64+\mathrm{x} * 32+\mathrm{x} * 4 \Rightarrow$
$\mathrm{x}:=\operatorname{SHL}(\mathrm{x}, 6)+\operatorname{SHL}(\mathrm{x}, 5)+\operatorname{SHL}(\mathrm{x}, 2)$

## 12 Local, Global,Inter-Proc. II

> | Original Code |
| :--- |

FUNCTION $P(X, n)$ : INT;
IF $\mathrm{n}=3$ THEN RETURN x [1]
ELSE RETURN X [n];
CONST $R=1$;
BEGIN
$\mathrm{K}:=3 ; \ldots$
IF $P(X, K)=X[1]$ THEN $\mathrm{X}[1]:=\mathrm{R} *(\mathrm{X}[1] * * 2)$

## After Local Opt

FUNCTION $P(X, n)$ : INT;
IF $\mathrm{n}=3$ THEN RETURN X [1]
ELSE RETURN X [n]
BEGIN
K : = 3; ...
IF $P(X, K)=X[1]$ THEN $\mathrm{X}[1]:=\mathrm{X}[1] * \mathrm{X}[1]$

## 13 Local, Global, Inter-Proc. III

|  |
| :--- |
| FUNCTION P $(\mathrm{X}, \mathrm{n}):$ INT; |
| IF $\mathrm{n}=3$ THEN RETURN X[1] Local Opt |
| ELSE RETURN X[n] |
| BEGIN |
| $\mathrm{K}:=3 ;$ |
| $\ldots$ |
| IF $\mathrm{P}(\mathrm{X}, \mathrm{K})=\mathrm{X}[1]$ THEN |
| $\quad \mathrm{X}[1]:=\mathrm{X}[1] * \mathrm{X}[1]$ |
|  |

## 14 Local, Global, Inter-Proc. IV

FUNCTION P (X,n): INT;
IF $\mathrm{n}=3$ THEN RETURN X[1]
ELSE RETURN X [n]
BEGIN
IF $P(X, 3)=X[1]$ THEN $\mathrm{X}[1]:=\mathrm{X}[1] * \mathrm{X}[1]$

After Inter-Procedural Opt
BEGIN
IF TRUE THEN
$\mathrm{X}[1]:=\mathrm{X}[1] * \mathrm{X}[1]$
After Another Local Opt
BEGIN
$\mathrm{X}[1]:=\mathrm{X}[1] * \mathrm{X}[1]$

- Delete P if it isn't used elsewhere. This can maybe be deduced by an inter-procedural analysis.


## Local Optimization

## 16 Local Optimization I

Transformations

- Local common subexpression elimination.
- Local copy propagation.
- Local dead-code elimination.
- Algebraic optimization.
- Jump-to-jump removal.
- Reduction in strength.
- On machine and/or interm. code.

1. Examine a "window" of instructions.
2. Improve code in window.
3. Slide window.
4. Repeat until "optimal".

## 17 Redundant Loads

- A naive code generator will generate the same address or variable several times. Peephole optimization over the generated code will easily remove these.

```
A := A + 1;
    \Downarrow
set A, %10
set A, %l1
ld [%l1], %l1
add %11, 1, %l1
st %11, [%10]
    \Downarrow
set A, %10
ld [%10], %l1
add %l1, 1, %l1
st %l1, [%10]
```


## 18 Jumps-to-jumps

- Complicated boolean expressions (with many and, or, nots) can easily produce lots of jumps to jumps. A peephole optimization pass over the generated code can remove these.

```
    if a < b goto L1
    ...
L1: goto L2
    ...
L2: goto L3
    \Downarrow
    if a < b goto L3
L1: goto L3
    ...
L2: goto L3
```


## 19 Algebraic Simplification

- Beware of numerical problems:

1. $(x * 0.00000001) * 10000000000.0$ may produce a different result than $(x * 1000.0)$ !

- FORTRAN requires that parenthesis be honored: $(5.0 * x) *(6.0 * y)$ can't be evaluated as $(30.0 * x * y)$.
- Note that multiplication is often faster than division.

$$
\begin{array}{rlll}
\mathrm{x}:=\mathrm{x}+0 ; & \Rightarrow & & \\
\mathrm{x}:=\mathrm{x}-0 ; & & & \\
\mathrm{x}:=\mathrm{x} * 1 ; & & \Rightarrow & \\
\mathrm{x}:=1 * 1 ; & \Rightarrow & \mathrm{x}:=1 \\
\mathrm{x}:=\mathrm{x} / 1 ; & \Rightarrow & \\
\mathrm{x}:=\mathrm{x} * * 2 ; & & \Rightarrow & \mathrm{x}:=\mathrm{x} * \mathrm{x} ; \\
\mathrm{f}:=\mathrm{f} / 2.0 ; & \Rightarrow & \mathrm{f}:=\mathrm{f} * 0.5 ;
\end{array}
$$

## 20 Reduction in Strength

- $\operatorname{SHL}(\mathrm{x}, \mathrm{y})=$ shift x left y steps.
- Multiplcation (and division) by a constant is a common operation. They can be replaced by cheaper sequences of shifts and adds.

```
x := x * 32 ;
    \Downarrow
x := SHL(x, 5);
x := x * 100 ;
    \Downarrow
x := x * (64 + 32 + 4)
    \Downarrow
x := x * 64 + x *32 + x *4
    \Downarrow
x := SHL(x,6) + SHL(x,5) +
    SHL (x,2)
```

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## Global Optimization

## 22 Global Optimization I

- Makes use of control-flow and data-flow analysis.
- Dead code elimination.
- Common subexpression elimination (local and global).
- Loop unrolling.
- Code hoisting.
- Induction variables.
- Reduction in strenght.
- Copy propagation.
- Live variable analysis.
- Uninitialized Variable Analysis.


## 23 Control Flow Graphs

- We perform our optimizations over the control flow graph of a procedure.



## 24 Common Sub-Expr. Elimination



## 25 Copy Propagation

- Many optimizations produce $X:=Y$.
- After an assignment $X:=Y$, replace references to $X$ by $Y$. Remove the assignment if possible.



## 26 Dead Code Elimination

- A piece of code is dead if we can determine at compile time that it will never be executed.



## 27 Induction Variables

- If $i$ and $j$ are updated simultaneously in a loop, and $j=i * c_{1}+c_{2}\left(c_{1}, c_{2}\right.$ are consants) we can remove one of them, and/or replace $*$ by + .



## 28 Code Hoisting

- Move code that is computed twice in different basic blocks to a common ancestor block.

$$
\begin{aligned}
& \text { IF } a<5 \text { THEN X :=A[i+3]+9; } \\
& \text { ELSE } X:=A[i+3] * 6 \text { END }
\end{aligned}
$$



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## Loop Unrolling

## 30 Loop Unrolling

FOR i := 1 TO 5 DO A[i]:=i END
$\mathrm{A}[1]:=1 ; \mathrm{A}[2] \stackrel{\Downarrow}{\Downarrow}:=2 ; \mathrm{A}[3]:=3$;
$\mathrm{A}[4]:=4 ; \mathrm{A}[5]:=5$;

```
FOR i := 1 TO n DO A[i] := i END
                        \Downarrow
i := 1;
WHILE i <= (n-4) DO
    A[i]:=i; A[i+1]:=i+1; A[i+2]:=i+2;
    A[i+3]:=i+3; A[i+4]:=i+4; i:=i+5;
END;
WHILE i<=n DO A[i]:=i; i:=i+1; END
```

- Loop unrolling increases code size. How does this effect caching?


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## Inter-procedural Optimizations

## 32 Inter-procedural Opt.

- Consider the entire program during optimization.
- How can this be done for languages that support separately compiled modules?


## Transformations

- Inline expansion
- Replace a procedure call with the code of the called procedure.
- Procedure Cloning
- Create multiple specialized copies of a single procedure.
- Inter-procedural constant propagation
- If we know that a particular procedure is always called with a constant parameter with a specific value, we can optimize for this case.


## 33 Inline Expansion I

Original Code: $\qquad$
FUNCTION Power (n, exp:INT):INT;
IF exp < 0 THEN result := 0;
ELSIF $\exp =0$ THEN result $:=1$;
ELSE result := n;
FOR i := 2 TO exp DO
result := result $* \mathrm{n}$;
END; END;
RETURN result;
END Power;
BEGIN X := 7; PRINT Power (X,2) END;

```
BEGIN
    X := 7;
        result := X;
        FOR i := 2 TO 2 DO
            result := result * X;
        END;
    PRINT result;
END
```


## 34 Inline Expansion II



## 35 Procedure Cloning

```
FUNCTION Power (n, exp:INT):INT;
    IF exp < O THEN result := 0;
    ELSIF exp = 0 THEN result := 1;
    ELSE result := n;
        FOR i := 2 TO exp DO
            result := result * n;
        END;
    RETURN result;
END Power;
```

BEGIN PRINT Power (X,2), Power (X,7) END;

Cloned Routines: $\qquad$
FUNCTION PowerO (n):INT; RETURN 1;
FUNCTION Power2 ( n ):INT; RETURN $\mathrm{n} * \mathrm{n}$;
FUNCTION Power3 (n):INT; RETURN $n * n * n$;

```
FUNCTION Power (n, exp:INT):INT;
    (* As before *)
```

```
BEGIN PRINT Power2(X), Power(X,7) END;
```


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## Machine Dependent vs. Machine Independent Optimization

## 37 Machine (In-)Dependent Opt.? I

- Optimizations such as inline expansion and loop unrolling seem pretty machine independent. You don't need to know anything special about the machine architecture to implement these optimizations, in fact, both inline expansion and loop unrolling can be applied at the source code level. (May or may not be true for inline expansion, depending on the language).
- However, since both inline expansion and loop unrolling normally increase the code size of the program, these optimizations do, in fact, interact with the hardware.


## 38 Machine (In-)Dependent Opt.? I

- A loop that previously might have fit in the instruction cache of the machine, may overflow the cache once it has been unrolled, and therefore increase the cache miss rate so that the unrolled loop runs slower than the original one.
- The unrolled loop may even be spread out over more than one virtual memory page and hence affect the paging system adversely.
- The same argument holds for inline expansion.

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## Example I

## 40 Example I/a - Loop Invariants

```
FOR I:= 1 TO 100 DO
    FOR J := 1 TO 100 DO
        FOR K := 1 TO 100 DO
            A[I][J][K] := (I*J)*K;
        END;
    END;
END
```

$\qquad$

```
FOR I:= 1 TO 100 DO
    T3 := ADR(A[I]);
    FOR J := 1 TO 100 DO
        T1 := ADR(T3[J]);
        T2 := I * J;
        FOR K := 1 TO 100 DO
            T1[K] := T2 * K
        END;
    END;
END
```


## 41 Example I/b - Strength Reduct.

```
FOR I:= 1 TO 100 DO
    T3 := ADR(A[I]);
    FOR J := 1 TO 100 DO
        T1 := ADR(T3[J]); T2 := I * J;
        FOR K := 1 TO 100 DO T1[K]:=T2*K END;
    END;
END
```

After strength reduction:

```
FOR I:= 1 TO 100 DO
    T3 := ADR(A[I]); T4 := I;
    FOR J := 1 TO 100 DO
        T1 := ADR(T3[J]);
        T2 := T4; (* T4 = I*J *)
        T5 := T2; (* Init T2*K *)
        FOR K := 1 TO 100 DO
            T1[K] := T5; T5 := T5 + T2;
        END;
        T4 := T4 + I;
    END; END
```

- T4 holds $\mathrm{I} * \mathrm{~J}: I, I+I, I+I+I, \cdots I * J . \mathrm{T} 5$ holds $\mathrm{T} 2 * \mathrm{~K}=\mathrm{I} * \mathrm{~J} * \mathrm{~K}$.


## 42 Example I/c - Copy Propagation

```
FOR I:= 1 TO 100 DO
    T3 := ADR(A[I]); T4 := I;
    FOR J := 1 TO 100 DO
        T1 := ADR(T3[J]); T2 := T4; T5 := T2;
        FOR K := 1 TO 100 DO
            T1[K] := T5; T5 := T5 + T2;
        END; T4 := T4 + I;
    END;
END
```

```
FOR I:= 1 TO 100 DO
    T3 := ADR(A[I]); T4 := I;
    FOR J := 1 TO 100 DO
        T1 := ADR(T3[J]); T5 := T4;
        FOR K := 1 TO 100 DO
            T1[K] := T5; T5 := T5 + T4;
        END;
        T4 := T4 + I;
    END;
END
```

- We replace T2 by T4.


## 43 Example I/d - Array Indexing

- Expand subscripting operations. Pascal array indexing turns into C-like address manipulation!

Expand Indexing:
VAR A:ARRAY[1..100,1..100,1..100] OF INT;

```
FOR I:= 1 TO 100 DO
    T3 := ADR(A) + (10000*I)-10000;
    T4 := I;
    FOR J := 1 TO 100 DO
        T1 := T3 +(100*J)-100;
        T5 := T4;
        FOR K := 1 TO 100 DO
            (T1+K-1)\uparrow := T5;
            T5 := T5 + T4;
        END;
        T4 := T4 + I;
    END; END
```


## 44 Example I/e - Array Indexing

```
T6 := ADR(A);
FOR I:= 1 TO 100 DO
    T4 := I;
    T7 := T6;
    FOR J := 1 TO 100 DO
        T5 := T4;
        T8 := T7;
        FOR K := 1 TO 100 DO
            T8\uparrow := T5;
            T5 := T5 + T4;
            T8 := T8 + 1;
        END;
        T4 := T4 + I;
        T7 := T7 + IO0;
```

END;
T6 := T6 + 10000;
End

## 45 Example I/f - Loop Unrolling

```
T6 := ADR(A);
FOR I:= 1 TO 100 DO
    T4 := I; T7 := T6;
    FOR J := 1 TO 100 DO
        T5 := T4; T8 := T7;
        FOR K := 1 TO 10 DO
            T8\uparrow := T5; T5 += T4; T8 ++;
            T8\uparrow := T5; T5 += T4; T8 ++;
            T8\uparrow := T5; T5 += T4; T8 ++;
            T8\uparrow := T5; T5 += T4; T8 ++;
            T8\uparrow := T5; T5 += T4; T8 ++;
            T8\uparrow := T5; T5 += T4; T8 ++;
            T8\uparrow := T5; T5 += T4; T8 ++;
            T8\uparrow := T5; T5 += T4; T8 ++;
            T8\uparrow := T5; T5 += T4; T8 ++;
            T8\uparrow := T5; T5 += T4; T8 ++;
        END;
        T4 := T4 + I; T7 := T7 + I00;
    END;
    T6 := T6 + 10000;
END
```

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## Example II

## 47 Example II/a - Inline Expansion

- ftp://cs.washington.edu/pub/pardo. The code has been simplified substantially...
- bitblt copies image region regions while performing an operation on the moved part.
- $s$ is the source, $d$ the destination, $i$ the index in the $x$ direction, $j$ the index in the $y$ direction.
- Every time around the loop we have to execute a switch (case) statement, which is very inefficient.
- Here we'll show how bitblt can be optimized by inlining. It's also amenable to run-time (dynamic) code generation. I.e. we include the code generator in the executable and generate code for bitblt when we know what it's arguments are.


## 48 Example II/b - Inline Expansion

```
#define BB_S (Oxc)
bitblt (mask_t m, word s, word d, int op)
{for (j=0; j<dy; ++j) {
    for (i=nw+1; i>0; --i) {
        switch (op) {
            case (0) : *d &= ~mask; break;
            case (BB_D&~BB_S) :
                *d ^= ((s &*d) & mask); break;
            case (~BB_S) :
                *d ^= ((~s ^ *d) & mask); break;
            /* Another 12 cases... */
            case (BB_X) : *d I= mask; break;
    }; d++;
    }; d++; s++;
}
}
main () {bitblt(mask,src,dest,...,BB_S);}
```


## 49 Example II/c - Inline Expansion

```
                                    Expanded Code
main () {
d = src; s=dst;
for (j=0; j<dy; ++j) {
    for (i=nw+1; i>0; --i) {
        switch (BB_S) {
            case (0) : *d &= ~mask; break;
            case (BB_D&~BB_S) :
                *d ^= ((s &*d) & mask); break;
            case (~BB_S) :
                *d ^= ((~s ^ *d) & mask); break;
            /* Another 12 cases... */
            case (BB_X) : *d |= mask; break;
        }; d++;
    }; d++; s++;
}
}
```


## 50 Example II/d - Inline Expansion

## After Dead Code Elim

```
main () {
    d = src; s=dst;
    for ( j=0; j<dy; ++j) {
        for (i=nw+1; i>0; --i) {
            d ^= ((s ^ *d) & mask);
            d++;
        };
        d++; s++;
```


# Summary 

## 52 Summary

- Read the Dragon book: 530-532, 585-602.
- Debugging optimized code: See the Dragon book. pp. 703-711.
- Difficult problems:
- Which transformations are actually profitable?
- How do we avoid unsafe optimizations?
- What part of the code should we optimize?
- How do we take machine dependencies (cache size) into account?
- At which level(s) do we optimize (source, interm. code, machine code)?
- How do we order the different optimizations?

